

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

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PATENT NO. : 7,474,663 B2

APPLICATION NO. : 10/501,749

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INVENTOR(S) : Natoli, et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Column 6, Line 7, in Claim 3, after “functionality” delete “is”.

In Column 6, Line 10, in Claim 4, after “claim 1” insert - - , - -.

In Column 6, Line 18, in Claim 6, after “comprising” insert - - : - -.

In Column 6, Line 49, in Claim 10, delete “device processor board” and insert - - Device Processor Board - -, therefor.

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